

RECONFIGURABLE NETWORKS ON CHIP

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Towards reconfigurable optical networks on chip I. O Connor, L. M. Bri re, E. Drouard, A. Kazmierczak, F. with reconfigurable targets, and where the value of D will be determined by: $D = \frac{nf}{n} \frac{1}{n} \frac{1}{D_e}$ where nf represents the ratio between the optical transmission frequency and the data arrival rate, n the ratio between the number of physical optical and metallic lines, n the number of Reconfigurable Networks on Chip: DRNoC architecture ...

Her research interests are focused on reconfigurable and control systems, networks on chip and wireless-sensor network. She has worked in several research projects with European and national funding, as well in projects for the industry. She is also involved in the center academic activity co-tutoring final degree projects and master thesis. [Reconfigurable Networks-on-Chip eBook by Sao-Jie Chen ...](#)

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However, metallic interconnect networks (networks-on-chip) can be very costly in terms of power and silicon area and can thus become a bottleneck in system on chip design.

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Reconfigurable computing brings immense flexibility to on-chip processing while network-on-chip has improved flexibility in on-chip communication. Integrating these two areas of research reaps the benefits of both and represents the promising future of multiprocessor systems-on-chip.

This book is the one of the first compilations written to demonstrate this future for network-on-chip design

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This book provides a comprehensive survey of recent progress in the design and implementation of Networks-

on-Chip. It addresses a wide spectrum of on-chip communication problems, ranging from **Reconfigurable Optical Networks-on-Chip** a reconfigurable optical network-on-chip can drastically improve the performance of different programs. It allows us to see that changing the placement of the nodes (depending on an incoming file), and adding frequencies to allow for wave-division-multiplexing (depending on the file and configuration) reduce wait time immensely and are definitely worth it. Now we will need to determine how to **Reconfigurable Networks-on-Chip | SpringerLink**

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A Cluster-based Reconfigurable Optical Network on Chip Design Luying Bai Huaxi Gu Yintang Yang Kun Wang Xidian University Xi'an, China

A Reconfigurable Analog VLSI Neural Network Chip A Reconfigurable Analog VLSI Neural Network Chip 759 3 inputs 'hideMa NiUYORS', inputs Figure 1:

Reconfigurability 7 Inputs neural networks provide a fast means of solving the problem.